

Atty. Dkt. No. 082259-0156

, IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Golshan

Title:

METHOD AND APPARATUS FOR

OPTICAL PROCESSING

Appl. No.:

09/630,883

Filing Date:

8/2/2000

Examiner:

Chang, A.

Art Unit:

2872

REPLY

Commissioner for Patents Box NON-FEE AMENDMENT Washington, D.C. 20231

Sir:

This paper is being filed responsive to the Office Action dated September 6, 2001, in which the Examiner has entered a restriction requirement.

REMARKS

In Section 1 of the Office Action, the Examiner has required Applicants to elect a single group of claims for prosecution on the merits to which the claims shall be restricted. The Examiner has grouped the claims as follows:

Group I

Claims 1-22 and 31-46, drawn to an optical logic circuit having

Boolean logic output, classified in class 359, subclass 577; or

Group II

Claims 23-30, drawn to a method for making a semiconductive

device, classified in class 438, subclass 21.

The Examiner states that the inventions are distinct, each from the other because of the following reasons: